

Form PTO-1449 (modified)

Atty. Docket N .

Serial N .

SILI:003US

09/707,757

List of Patents and Publications for Applicant's

Applicant

Curtis L. Ratzlaff, John F. Cr ix and Robert J nes

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Filing Date:

November 7, 2000

Group:

2122

U.S. Patent Documents

See Page 1

Foreign Patent Documents

See Page 1

Other Art

See Page 1

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
M41	A1	5,878,053	3-2-99	Koh et al.	371	22.1	6-9-97
↓	A2	5,920,484	6-6-99	Nguyen et al.	364	489	12-2-96
↓	A3	6,072,945	6-6-00	Aji et al.	395	500.06	6-26-97

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
—	—	—	—	—	—	—	—

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
M41	C1	Anirudh and O'Brien, "Realizable reduction for RC interconnect circuits," ICCAD, 204-207, 1999.
↓	C2	Kamon et al., "Interconnect Parasitic Extraction in the Digital IC Design Methodology," Proceedings of the International Conference on Computer-Aided Design, pages 223-230, 1999.

RECEIVED

MAR 27 2003

Technology Center 2100

25276466.1

EXAMINER:

DATE CONSIDERED:

07/17/03

EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

INFORMATION DISCLOSURE STATEMENT — PTO-1449 (MODIFIED)